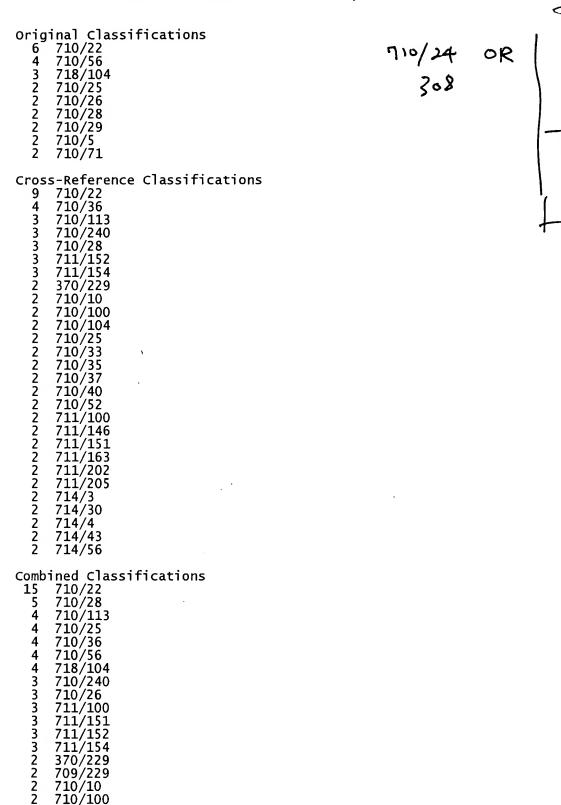
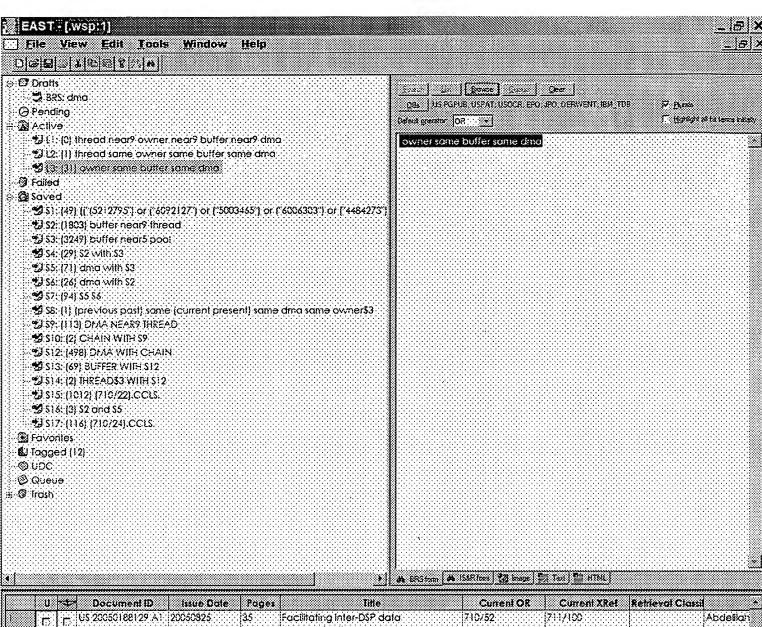
10682410_CLS1.txt Most Frequently Occurring Classifications of Patents Returned From A Search of 10682410 on June 14, 2005



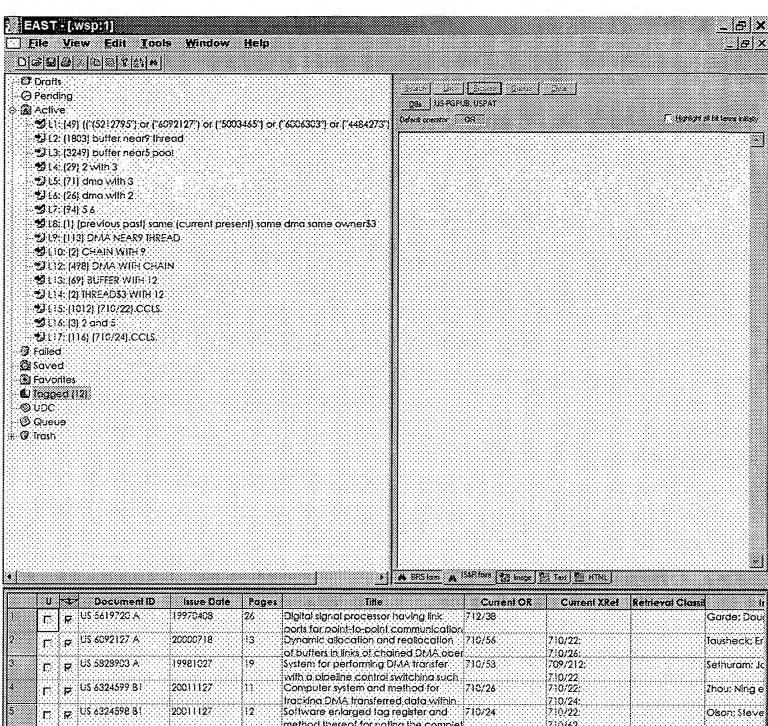
10682410_CLS1.txt

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2	710/200 710/244 710/29
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2	711/163 711/202
2	711/205 714/3
2	714/30 714/4
2	714/43 714/56



U	**	→ Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	il <u>*</u>
r	г	US 20050188129 A1	20050825	35	Facilitating Inter-DSP data communications	710/52	711/100		Abdellah
Г	г	US 20050080943 AT	20050414		Method and apparatus for efficient sharing of DMA resource	710/22			Pafumi, Jr
г	г	US 20040148382 A1	20040729	126	Language for handling network	709/223			Narad, Cl
г	г	US 20040143655 A1	20040722	φì	Accessing transmission control protocol (ICP) seaments	709/223			Narad, C
г	Г	US 20040073738 A1	20040415	15	Scalable efficient I/O port protocol	710/308	711/147		Kessler, Ri
г	Г	US 20040042483 A I	20040304	28	System and method for ICP attload	370/463	370/419		≘izur, Uri €
Г	Г	US 20040042458 A.I	20040304	28	System and method for handling out-of-order frames	370/394	370/389		Ezu, Uri
г	г	US 20030061332 A1	20030327	64	Multiple consumer-multiple producer rings	709/223			Narad, Cl
г	r	US 20030046423 A1	20030306	90	Programmable system for processing of partitioned network intrastructure	709/238			Narad: Ci
r	r	US 20030005103 A1	20030102	93	Cumulative status of arithmetic operations	709/223	709/201		Norad, Cl
г	Г	US 20020013886 A1:	20020131	18	Multiprocessor system	711/130	711/147		Higuchi: T
Г	Γ	US 6889278 B1	20050503	17	Method and apparatus for fast acknowledgement and efficient servi	710/266	710/261: 710/267		Hoerl o r; J
г	r	US 6859841 82	20050222	83	Programmable system for processing or partitioned network infrastructure	709/236			Narad: Ci
г	г	US 6738836 E1	20040518	16		710/22	710/105; 710/107;		Kessler: Ri
г	г	US 6701338 82	20040302	[4, 4, 4] 4] 4] 4] 4] 4]	Cumulative status of crithmetic operations	708/525	709/223		Narad; C
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	- 1	٦	US 5619720 A	19970408	26	Digital signal processor having link	712/38		Garde; Dau
I	\Box					ports for point-to-point communication			
∭ r	- :	₽	US 6092127 A	20000718	13	Dynamic allocation and reallocation	710/56	710/22:	Tausheck; E
						of buffers in links of chained Dt/IA one		710/26:	
∭ r	4	₽	US 5828903 A	19981027	19	System for performing DMA transfer	710/53	709/212:	Sethuram:
 						with a plactine control switching such		710/22	
r	-	7	US 6324599 B1	20011127	m	Computer system and method for	710/26	710/22;	Zhou; Ning
<u> </u>						tracking DNA transferred data within		710/24:	
₩ г	÷.	₽	US 6324598 81	20011127	12	Software enlarged tag register and	710/24	710/22	Obon; Sleve
						method thereof for noting the complet		710/62	
∭ r	٠.	Ø	US 5850570 A	19981215	3	Communication apparatus for	710/24	710/17;	Shoji; Fumio
						performina data transfer utilizina direct	-	710/18:	
∭ r	- }	₽	US 5933654 A	19990803	4	Dynamic buffer fracturing by a DMA	710/23	710/22	Galdun; Da
						controller	**************************************		
г	•	Þ	US 20050033874 A1	20050210	10:::::	Direct memory access using memory	710/22		Futral, Willia
						descriptor list			
∭ r	-	₽	US 6718405 32	20040406	9	Hardware chain puil	710/22	710/24:	Rogers: Jeff
*** 1							<u> </u>	710/308:	
∭ r	-::	₽	US 5781799 A	19980714	16	DMA controller arrangement having	710/22		Leger: Gea
				. [.] .] .] .] .] .] .] .] .]		aturality of DMA controllers and buffer			
₩ r		₽	US 20040267982 A1	20041230	17	Read/write command buffer pool	710/52		Jackson. The
 						resource management using read-pat			
∭ r		₽	US 6336150 B1	20020101	30	Apparatus and method for enhancing	710/5	710/18:	Elis: Jackso!
	ાં					data transfer rates using transfer contr		718/33:	

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